MMA-283136-R5

28-31GHz 4W MMIC Power Amplifier

Data Sheet

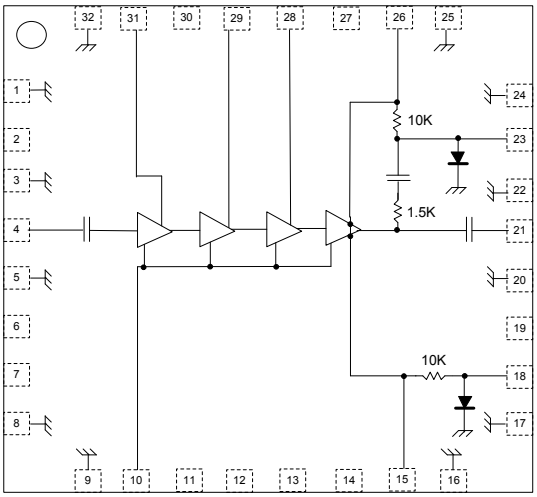
Old package not recommended for new designs

Features:

- **Frequency Range: 28 - 31 GHz**
- **P1dB: +36 dBm**
- **IM3 Level: -35 dBc @Po=26dBm/tone**
- **Gain: 22 dB**
- **Vdd = 5 to 6V**
- **Idsq = 1200 to 3000mA**
- **Input and Output Fully Matched to 50 Ω**
- **Integrated Output Power Detector**

Applications:

- **P2P Radio**
- **V-sat**



Functional Block Diagram

Description:

The MMIC is a high power amplifier MMIC in a surface mount package designed for use in transmitters that operate at frequencies between 28GHz and 31GHz. In the operational frequency band, it provides 36dBm of output power (P-3dB) and 22dB of small-signal gain. This MMIC is also optimized for high linearity applications. This MMIC provides IM3 level of -35dBc at Pout=26dBm/tone when biased under Vds=5V, Idsq=3000mA.

Absolute Maximum Ratings: *(Ta= 25 ℃)**

SYMBOL	PARAMETERS	UNITS	Min.	Max.
Vds	Drain-Source Voltage	V		6.5
Vg	Gate-Source Voltage	V	-2.1	0
Ig	First Gate Current	mA	-17	17
Pd	Power Dissipation	W		24
Pin max	RF Input Power	dBm		20
Toper	Operating Temperature	ºC		-40 to +85
Tch	Channel Temperature	ºC		+150
Tstg	Storage Temperature	ºC		-55 to +150
Tmax	Max. Assembly Temp (20 sec max)	ºC		+250

*Operation of this device above any one of these parameters may cause permanent damage.

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Electrical Specifications: *Vds=6V, Vgs=-0.85V, Idsq=2000mA, Ta=25 ℃ Z0=50 ohm*

Parameter	Units	Typical Data
Frequency Range	GHz	28-31
Gain (Typ / Min)	dB	22 / 20
Gain Flatness (Typ / Max)	+/-dB	2.5 / 3
Input RL(Typ/Max)	dB	10/8
Output RL(Typ/Max)	dB	10/8
Output P1dB(Typ/Min)	dBm	35/34
Output P3dB(Typ/Min)	dBm	36/35
IM3 Level (1)	dBc	-40
Thermal Resistance	ºC/W	3.8
Operating Current at P1dB(Typ / Max)	mA	2500 / 3000

(1) Output IP3 is measured with two tones at output power of 20 dBm/tone separated by 20 MHz.

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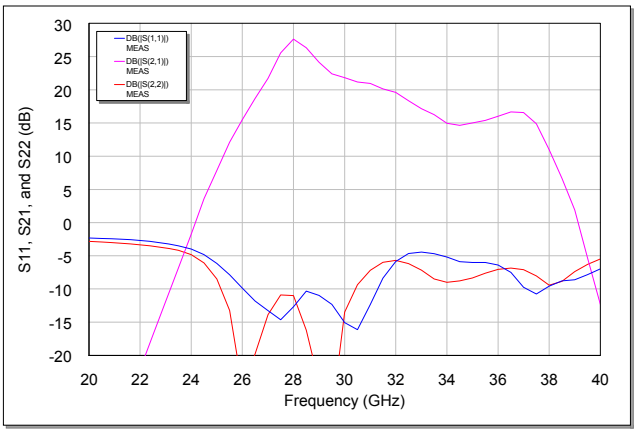
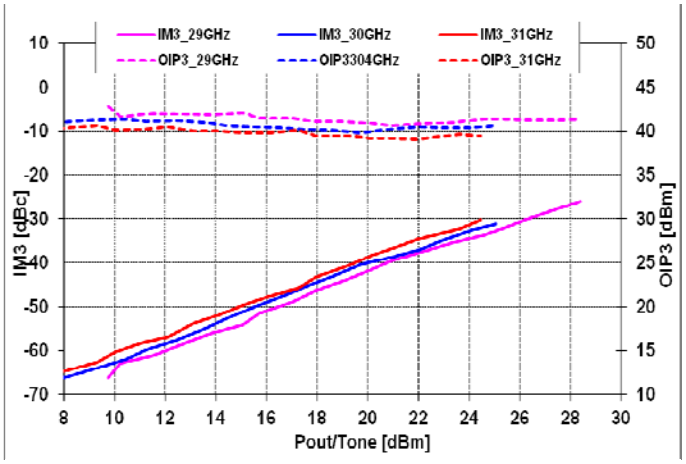
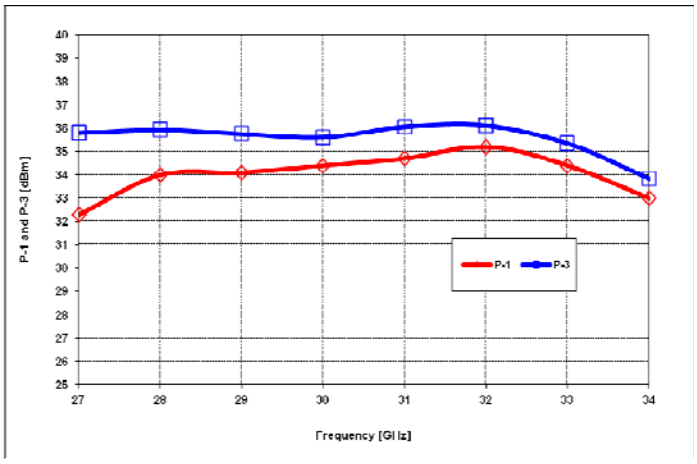
Typical RF Performance: *Vds=6V, Vgsq=-0.85V, Idsq=2000mA, Z0=50 ohm, Ta=25 ºC*



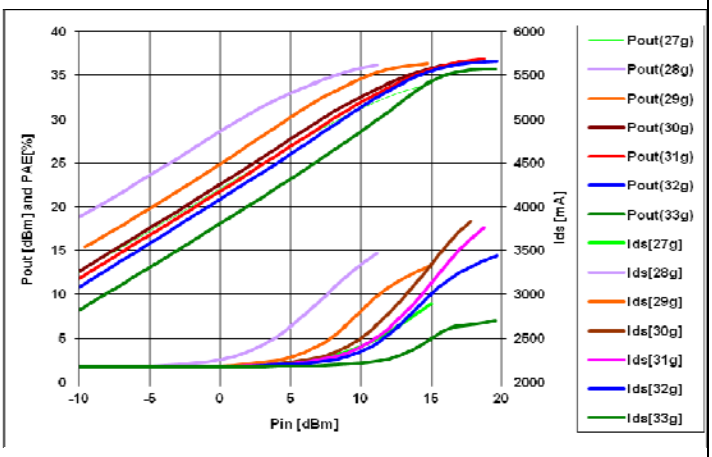
S11, S21, and S22 vs. Frequency



IM3 level [dBc] vs. Output power/tone [dBm]



P-1 and P-3 vs. Frequency



Po(dBm), and Ids(mA) vs. Pin(dBm)

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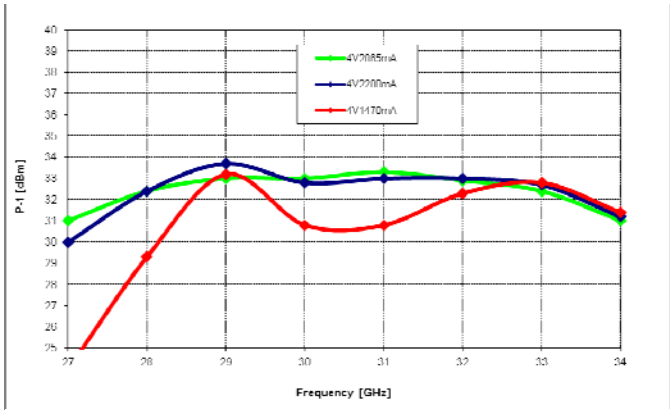
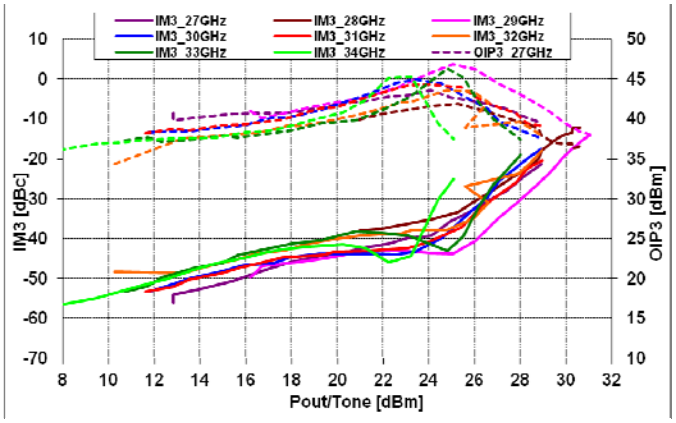
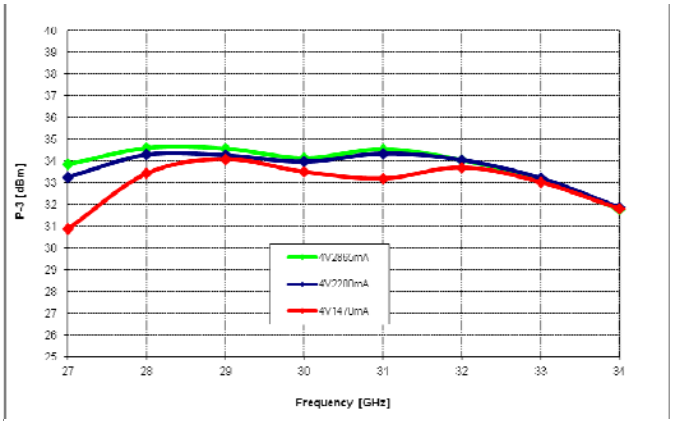
Typical Bias dependent RF Performance: Vds=4V



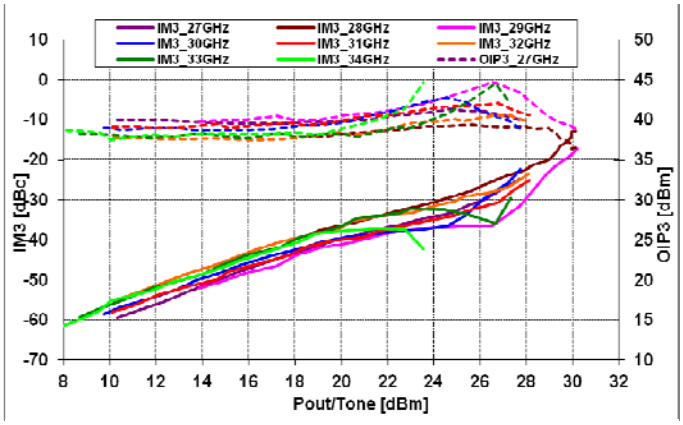
Bias dependent P1 vs. Frequency



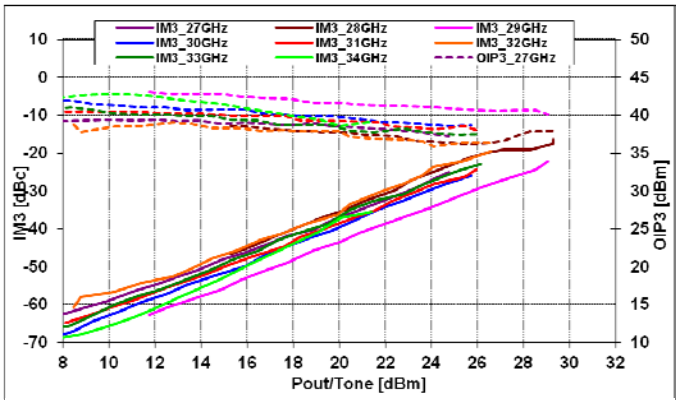
@Vds=4V, Idsq=2.8A



Bias dependent P-3 vs. Frequency



@Vds=4V, Idsq=2.2A



@Vds=4V, Idsq=2.2A

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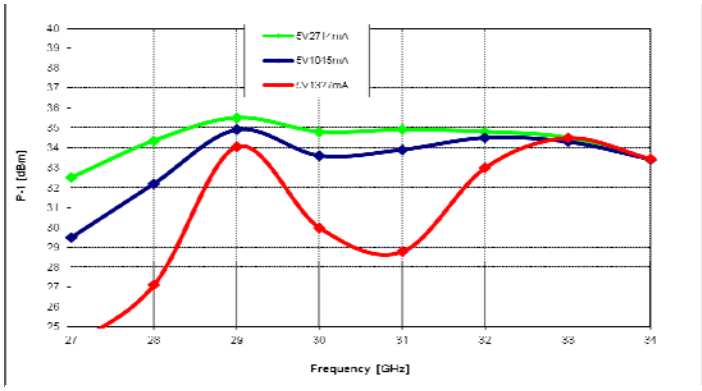
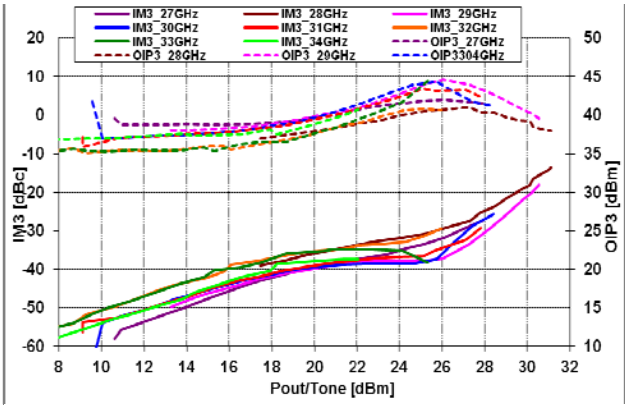
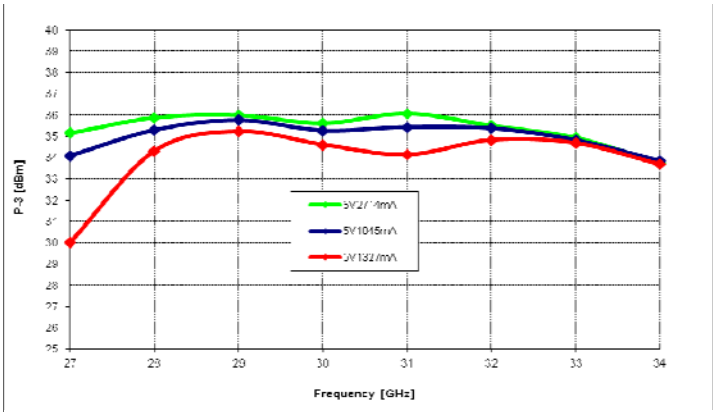
Typical Bias dependent RF Performance: Vds=5V



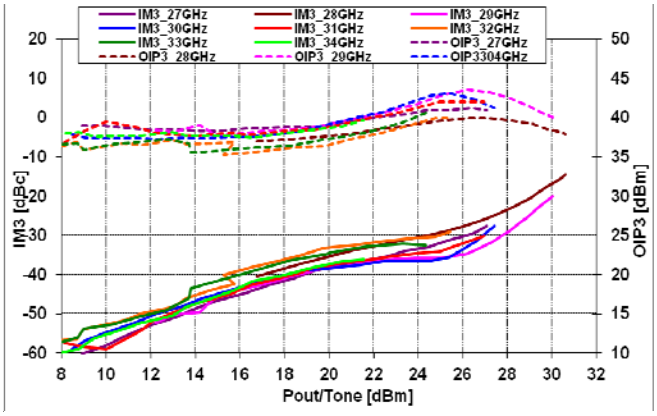
Bias dependent P1 vs. Frequency



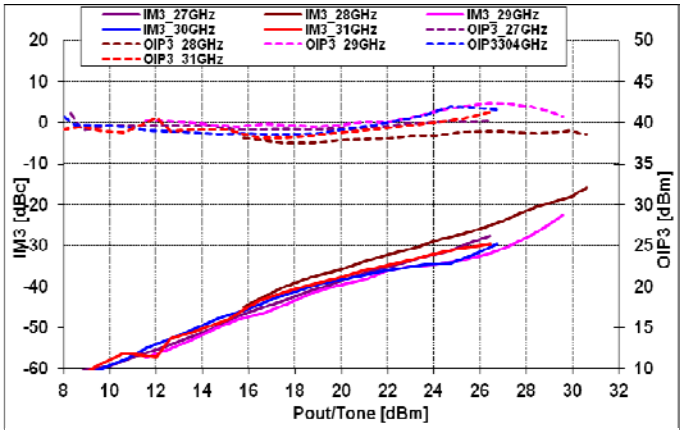
@Vds=5V, Idsq=3A



Bias dependent P-3 vs. Frequency



@Vds=5V, Idsq=2.6A



@Vds=5V, Idsq=1.5A

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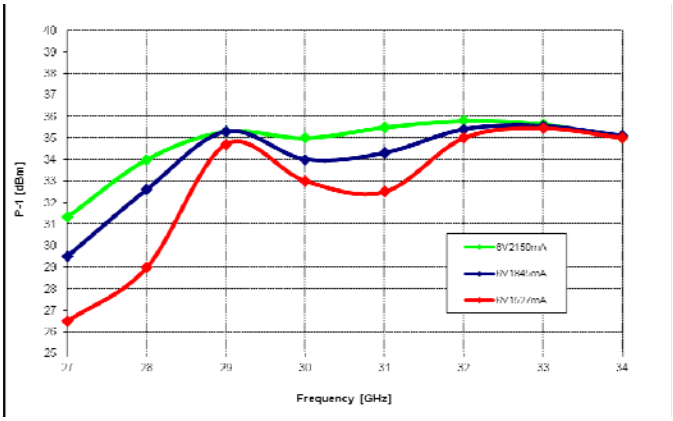
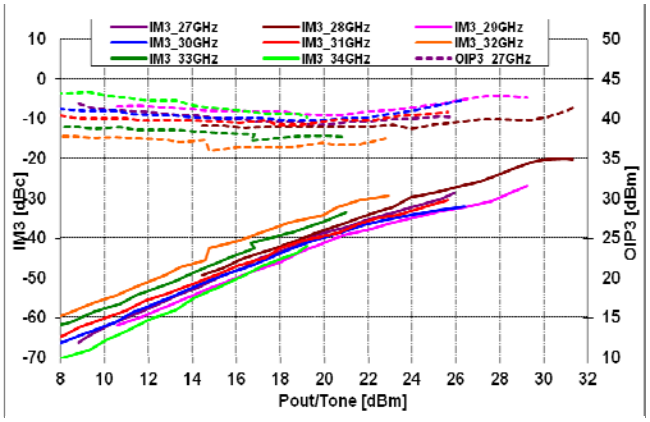
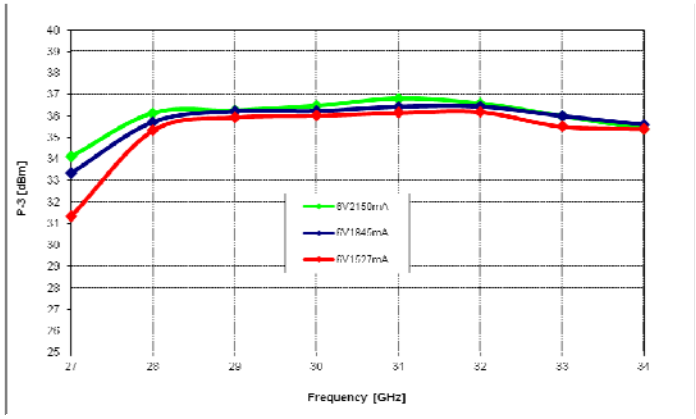
Typical Bias dependent RF Performance: Vds=6V



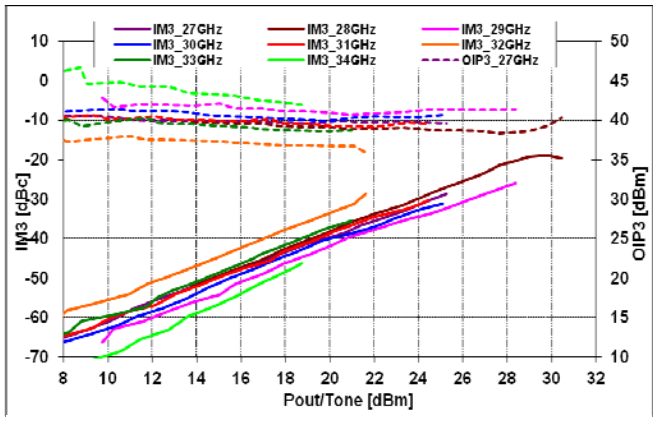
Bias dependent P1 vs. Frequency



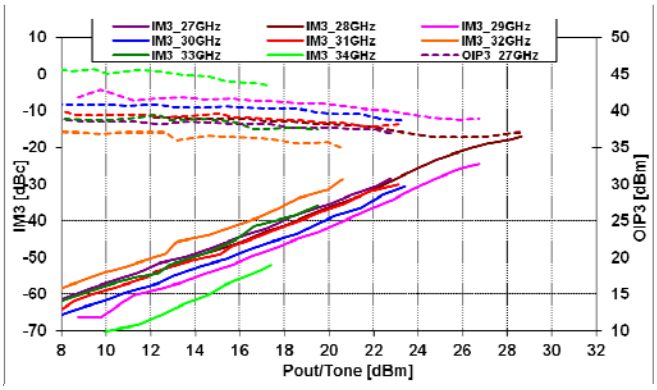
@Vds=6V, Idsq=2.5A



Bias dependent P-3 vs. Frequency



@Vds=6V, Idsq=2A



@Vds=6V, Idsq=1.5A

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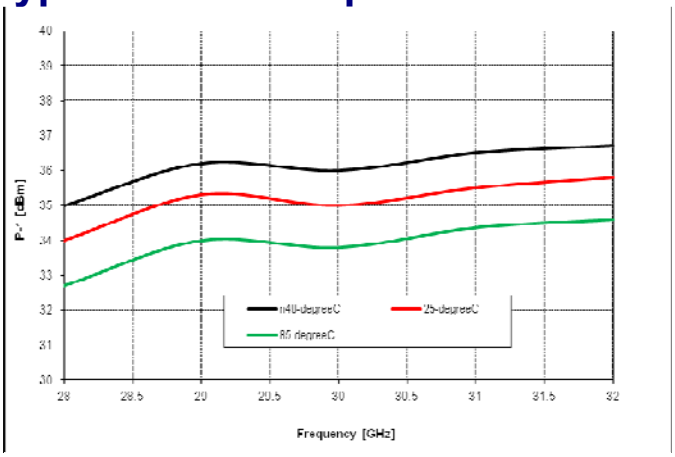
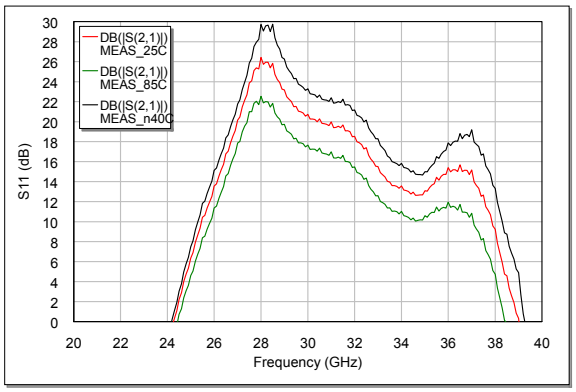
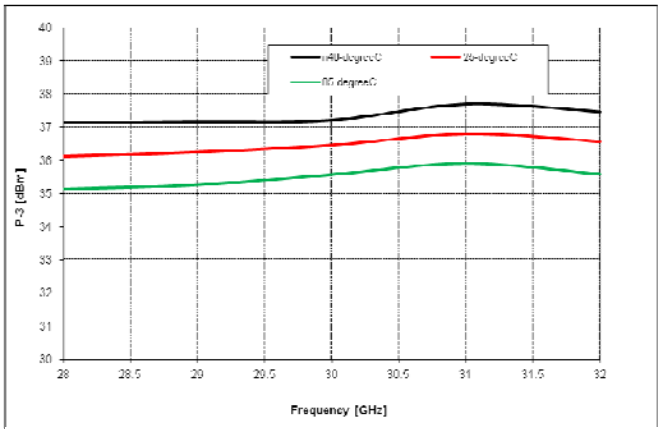
Typical Over Temperature Performance: *Vds=6V, Ids=2000mA, Z0=50 ohm, Ta=-40, 25, and 85 ºC*



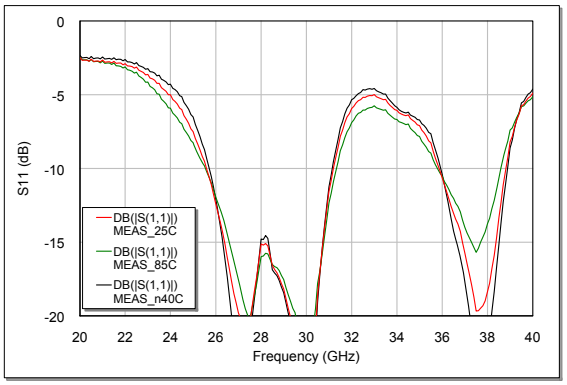
P1 over temperature



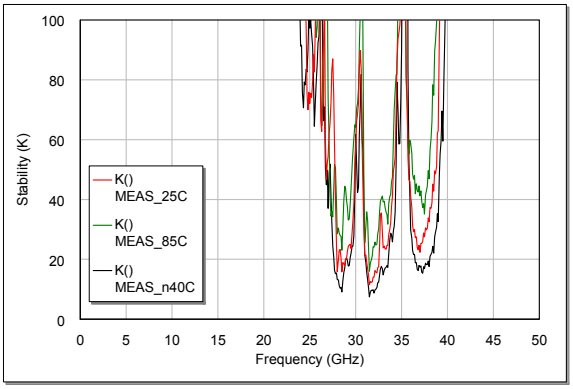
S21(dB)



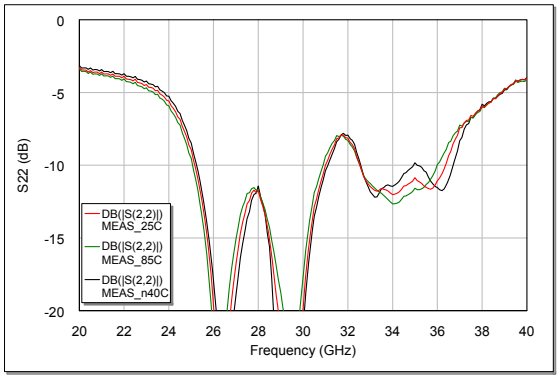
P-3 over temperature



S11(dB)



K-factor vs. Frequency



S22(dB)

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Applications

The **MMA-283136-R5** MMIC power amplifier is designed for use as a power stage amplifier in microwave transmitters. It is ideally suited for 28 to 31GHz band V-sat transmitter applications requiring excellent saturated output power and linearity performance. This amplifier is provided as a 5x5mm QFN package, and the packaged amplifier is fully compatible with industry standard high volume surface mount PCB assembly processes.

Biasing and Operation

The recommended bias conditions for best performance for high power applications the **MMA-283136-R5** are VDD = 6.0V, Idsq = 2000mA. Performance improvements are possible depending on applications. For high linearity requirement at higher output power up to 27dBm/tone, recommended bias conditions are Vdd=5V, Idsq=3000mA. The drain bias voltage range is 5 to 6V and the quiescent drain current biasing range is 1200mA to 3000mA. A single DC gate supply connected to Vg will bias all the amplifier stages. Muting can be accomplished by setting Vg to the pinch-off voltage (Vp=-1.8V). The gate voltage (Vg) should be applied prior to the drain voltages (Vd1, Vd2, Vd3, and Vd4) during power up and removed after the drain voltages during power down. The RF input and output ports are DC decoupled internally. Typical DC supply connection with bi-passing capacitors for the **MMA-283136-R5** is shown in following pages.

Assembly Techniques

GaAs MMICs are ESD sensitive. ESD preventive measures must be employed in all aspects of storage, handling, and assembly. MMIC ESD precautions, handling considerations, die attach and bonding methods are critical factors in successful GaAs MMIC performance and reliability.

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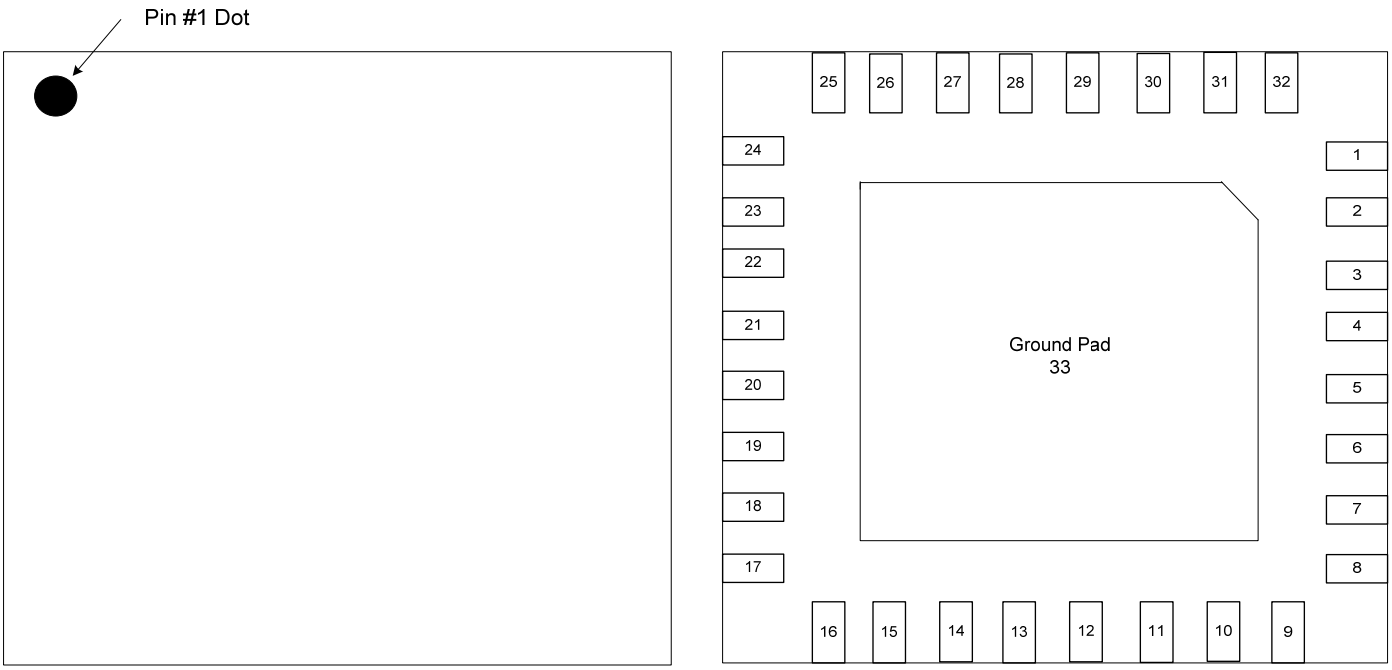
Package Pin-out:



Pin	Description
4	RF Input
21	RF Output
10	Vg
31	Vd1
29	Vd2
28	Vd3
15, 26	Vd4
18	DET_Reference
23	DET_Output
1, 3, 5, 8 ,9, 16, 17, 20, 22, 24, 25, 32, 33	Ground
2, 6, 7, 11, 12, 13, 14, 19, 27, 30	N/C

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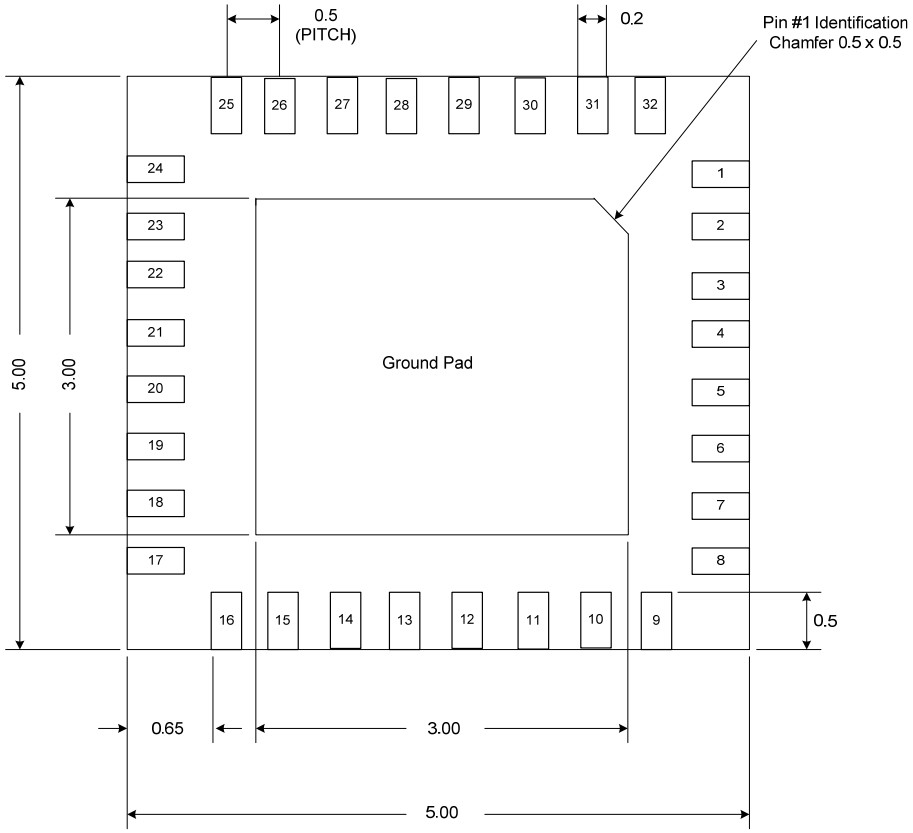
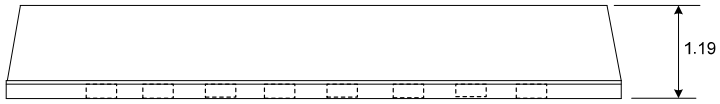
Mechanical Information:



BOTTOM VIEW



SIDE VIEW

The units are in [mm].

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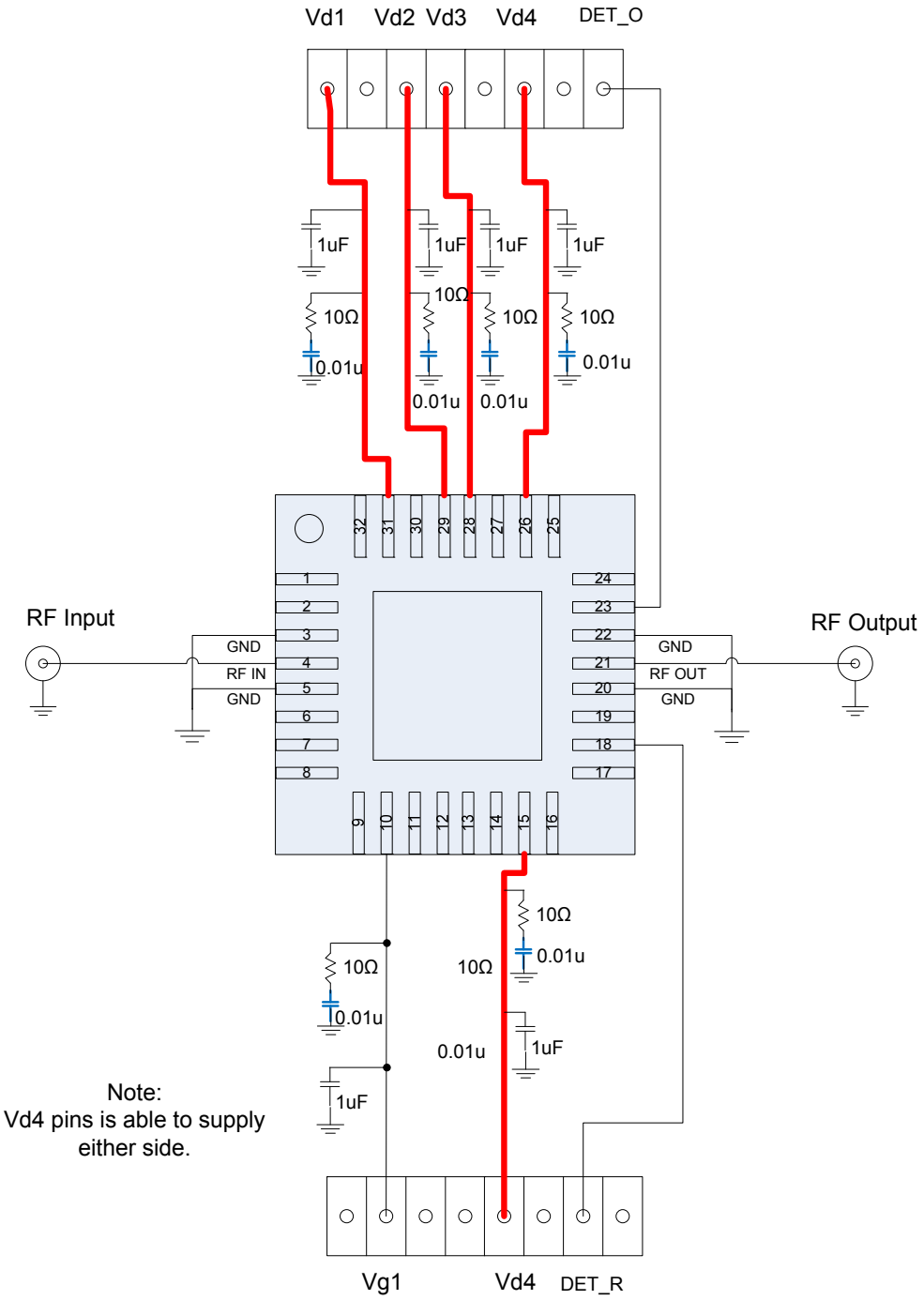
Application Circuit:



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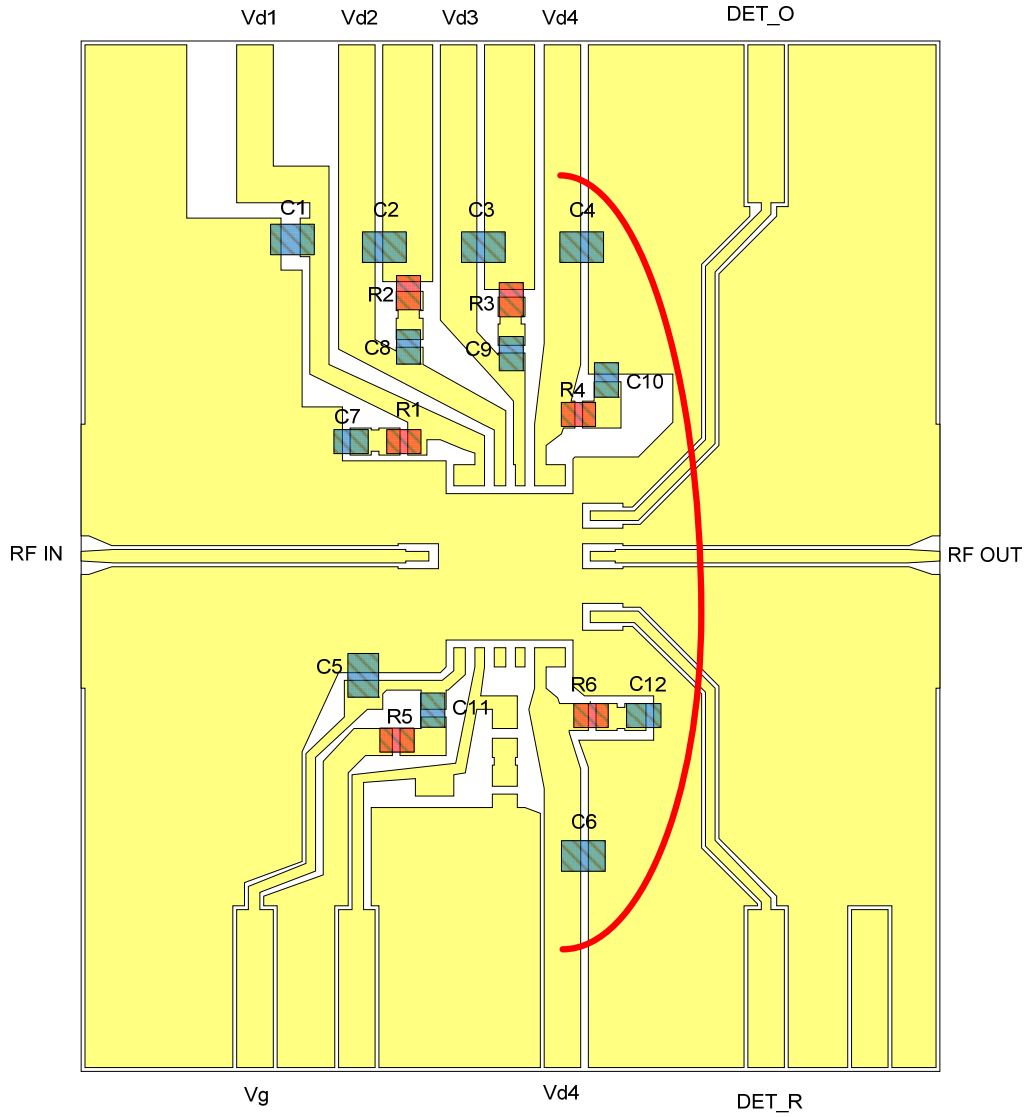
Recommended Application Board Design:

Board Material is 10mil (Dielectric) thickness Rogers 4350B with 0.5oz cupper clads.
Board is soldered on a gold plated solid cupper block and adequate heat-sinking is required for 16.8W total power dissipation.



Part	Description
C1, C2, C3, C4, C5, C6	1uF capacitor (0603)
C7, C8, C9, C10, C11, C12	0.01uF Capacitor (0402)
R1, R2, R3, R4, R5, R6	10Ω Resistor (0402)

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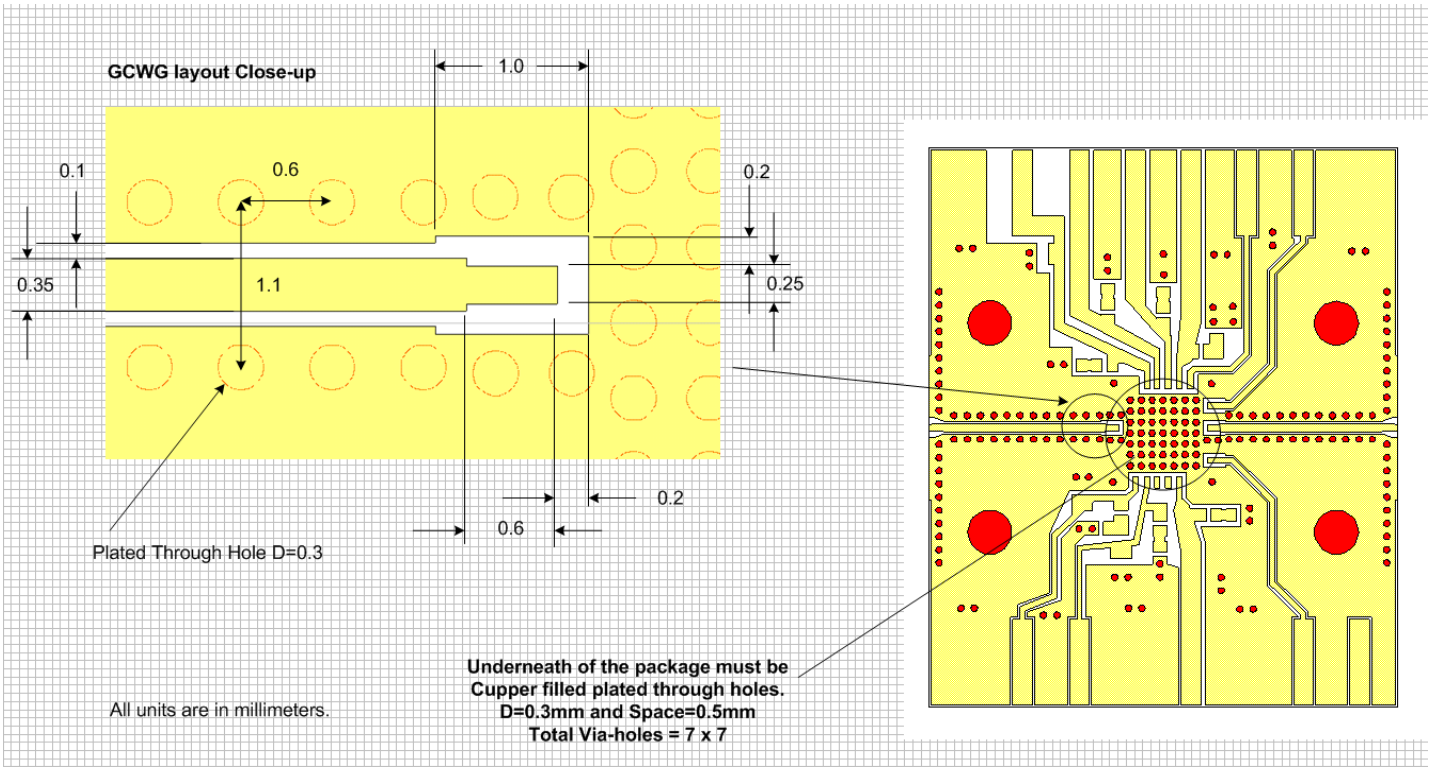
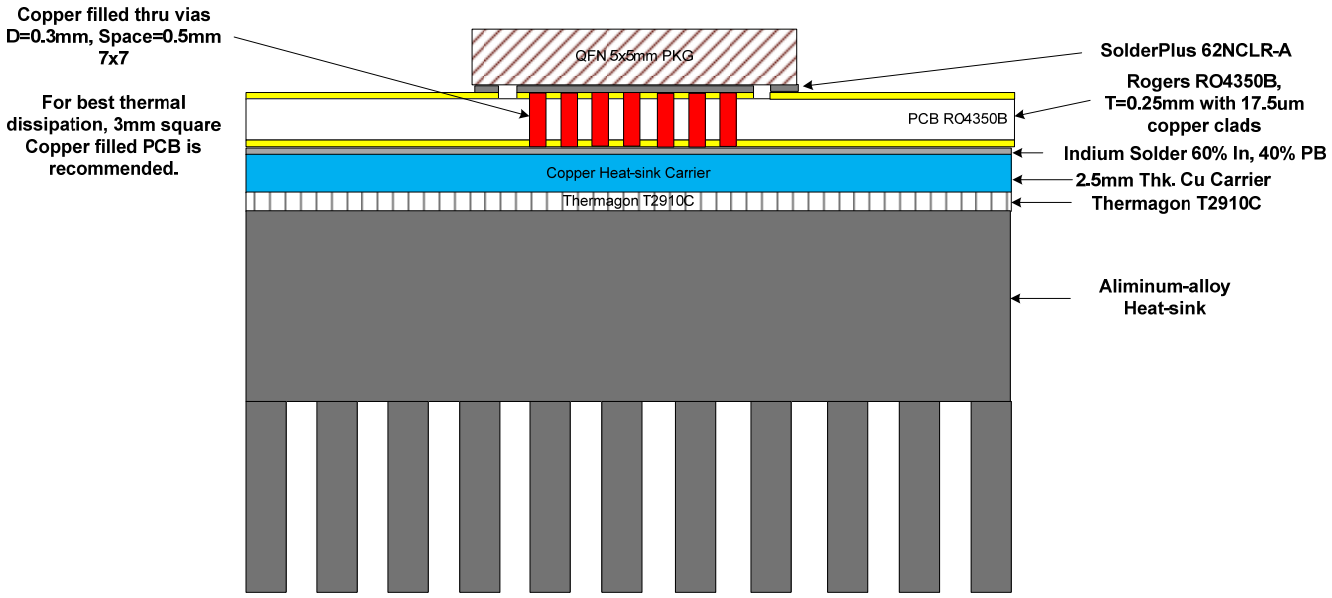
Recommended Application Board Design:

Board Material is 10mil (Dielectric) thickness Rogers 4350B with 0.5oz cupper clads. The board material and mounting pattern, as defined in the data sheet, optimizes RF performance and is strongly recommended. An electronic drawing of the land pattern is available upon request from *MwT* Sales & Application Engineering.





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